



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: James W. Jones
Assignee: Calix Networks, Inc.
Title: Asynchronous Receive and Transmit Packet Crosspoint
Serial No.: 09/874,395 Filing Date: June 4, 2001
Examiner: Habte Mered Group Art Unit: 2662
Docket No.: CLX021 US Confirmation No: 4480

Santa Clara, California
March 15, 2005

MAIL STOP RCE
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, §1.97 and §1.98, the Applicants submit for consideration in the above-identified patent application the document listed on the accompanying Form PTO-1449. Copies of references numbered 26-52 are submitted herewith. The Examiner is requested to make these documents of record. The remaining references are not attached hereto, because these references are issued patents or publications which are readily available in the U.S. Patent and Trademark Office.

In addition, Applicants submit for the Examiner's consideration, the prosecution histories of the following co-owned applications/patents, cited by serial number, first named inventor and filing date. The Applicants presume that the Examiner has access to and will review the cited applications/patents and the files thereof for any office actions, amendments or other materials that may be relevant to the patentability of the claims of the present application.

Citation of these prosecution histories (including the arguments against patentability advanced by Examiners in their respective Office Actions and the Applicants' arguments in the corresponding Amendments) is in accordance with the recent case DAYCO PRODUCTS, INC. v. TOTAL CONTAINMENT, INC., 02-1497, decided May 23, 2003 by the Court of

SILICON VALLEY
PATENT GROUP LLP
2350 Mission College Blvd.
Suite 360
Santa Clara, CA 95054
(408) 982-8200
FAX (408) 982-8210

Appeals for the Federal Circuit. The Examiner is presumed to be knowledgeable about the current case law, including the above-mentioned Dayco case. However, if the Examiner needs a copy of the Dayco case, please call the undersigned.

For any of the following U.S. patent application(s) that are currently pending, the Applicants further presume that the Examiner will consider any future office actions, amendments or other materials in the file thereof that may be relevant to the patentability of the claims herein. **If the Applicants' understanding in this regard is not correct, please notify the undersigned so that copies of any such documents can be submitted to the Examiner.**

	Serial No.:	First Named Inventor	Date:
1.	10/199,996	M. Vishnu	07/19/02
2.	09/874,904	Jason Dove	06/04/01
3.	09/874,352	Jason Dove	06/04/01
4.	09/874,348	Ying Zhang	06/04/01
5.	09/874,402	James W. Jones	06/04/01

Applicants would appreciate the Examiner initialing and returning the Form PTO-1449, indicating that the information has been considered and made of record herein.

The information contained in this Information Disclosure Statement is to the best of my knowledge and is not to be construed as a representation that: (i) a complete search has been made; (ii) additional information material to the examination of this application does not exist; (iii) the information, protocols, results and the like reported by third parties are accurate or enabling; or (iv) the above information constitutes prior art to the subject invention.

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Respectfully submitted,



Omkar K. Suryadevara
Attorney for Applicants
Reg. No. 36,320

SILICON VALLEY
PATENT GROUP LLP
2350 Mission College Blvd.
Suite 360
Santa Clara, CA 95054
(408) 982-8200
FAX (408) 982-8210

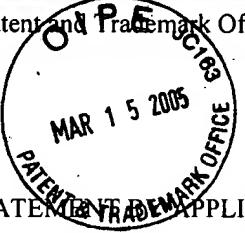
<p style="text-align: center;">U.S. Department of Commerce, Patent and Trademark Office INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)</p>		Application No.:	09/874,402
		Filing Date:	June 4, 2001
		First Named Inventor:	Jason Dove
		Group Art Unit:	2662
		Examiner Name:	Unknown
		Confirmation No.:	4498
		Attorney Docket No.:	CLX023 US

U.S. Patent Documents							
*Examiner Initials		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
	1.	6,195,355	02/2001	Demizu	370	397	
	7.	6,181,694	01/2001	Pickett, Scott K.	370	458	
	3.	5,987,026	11/1999	Holland, Peter	379	353	
	4.	6,014,431	01/2000	McHale et al.	379	93.07	
	5.	5,365,521	11/1994	Ohnishi et al.	370	352	
	6.	5,781,320	07/1998	Byers, Charles Calvin	370	395.51	
	7.	6,798,784	09/2004	Dove et al.	370	463	
	8.	6,262,986	Jul-01	Oba et al.	370	399	
	9.	6,072,800	Jun-00	Lee	370	412	
	19.	6,014,367	Jan-00	Joffe	370	230	
	11.	5,455,826	Oct-95	Ozveren et al.	370	60	
	12.	5,577,035	Nov-96	Hayter et al.	370	60	
	13.	5,500,858	Mar-96	McKeown	370	60	
	14.	5,923,644	Jul-99	McKeown et al.	370	230	
	15.	6,160,812	Dec-01	Bauman et al.	370	416	
	16.	6,327,253	Dec-01	Frink	370	260	
	17.	6,134,217	Oct-00	Stiliadis et al.	370	232	
	18.	5,982,771	9 Nov. 1999	Caldara et al.	370	389	
	19.	6,501,731	Dec-02	Chong et al.	370	230.1	
	20.	6,389,480	May-02	Kotzur et al.	709	249	
	21.	5,604,867	Feb-97	Harwood, Michael J.	709	233	
	22.	6,385,678	Feb-02	Jacobs et al.	710	113	
	23.	5,119,367	Jun-92	Kawakatsu et al.	370	232	
	24.	5,710,549	Jan-98	Horst et al.	340	825.5	
	25.	5,295,135	Mar-94	Kammerl, Anton	370	233	

Examiner:

Date Considered:

* Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication with applicant.

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		Confirmation No.:	4498
		Attorney Docket No.:	CLX023 US

Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)		
26.	PCT/US02/17515 PCT Search Report, dated 12 Dec. 2002, 1 page	
27.	PCT/US02/17515 Int'l Preliminary Examination Report, dated 12 Nov. 2003, 7 pages	
28.	Office Action dated September 28, 2004, (US Patent Application 10/199,996) (8 pages)	
29.	Response to Amendment dated September 28, 2004, (US Patent Application 10/199,996), filed January 24, 2005, (8 pages)	
30.	Office Action dated September 28, 2004 in EP Application No. 03254534.5-2416 based on US Patent Application 10/199,996 (5 pages total excluding cover sheet)	
31.	Search Report dated October 15, 2003 in EP Application No. 03254534.5-2416 based on US Patent Application 10/199,996 (2 pages total excluding cover sheet)	
32.	"The iSLIP scheduling algorithm for input-queued switches," by N. W. McKeown in IEEE/ACM Transactions on Networking, vol. 7, no. 2, April 1999	
33.	T. Anderson, S. Owicki, J. Saxe and C. Thacker, "High Speed Switch for Local Area Networks", ACM Transactions on Computer Systems, vol. 11, no. 4, Nov. 1993 pp. 1-13	
34.	N. W. McKeown, M. Izzard, A. Mekkittikul, W. Ellersick and M. Horowitz, "The tiny tera: A packet switch core", Hot Interconnects V., August 1996, pp. 1-13	
35.	A. Parekh, R. Gallager, "A Generalized Processor Sharing Approach To Flow Control in Integrated Services Networks: The Multiple Node Case", IEEE/ACM Transaction On Networking, VOL. 2, NO. 2, APRIL 1994, pp. 136-151	
36.	A. Parekh, R. Gallager, "A Generalized Processor Sharing Approach To Flow Control in Integrated Services Networks: The Single Node Case", IEEE/ACM Transaction On Networking, Vol. 1, No. 2, JUNE 1993, pp. 344-357	
37.	D. Stiliadis, A. Varma, "Efficient Fair-Queueing Algorithms for Packet-Switched Networks", IEE/ACM Transaction On Networking, Vol. 6, No. 2, 1998, Article No. 27473, pp. 1-11 and B.1-B.2	
38.	M. Goureau, S. Kollyopoulos, S. Rao, "Scheduling Algorithms for Input-Queued Switches: Randomized Techniques and Experimental Evaluation", IEEE/Infocom 2000, pp. 1634-1643	
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41.	I. Stoica, S. Shenker, H. Zhang, "Core-Stateless Fair Queueing: Achieving Approximately Fair Bandwidth Allocations in High Speed Networks", Http://www-2.cs.cmu.edu/~istoica/sig98talk/ , 1998, pp1-20	
42.	N. McKeown, "Scheduling Algorithms for Input-Queued Cell Switches", © 1995, pp. 1-119	
43.	R. Schoenen, "An Architecture Supporting Quality-of-Service in Virtual-Output-Queued Switches", iEICE Transaction Communications, Vol. E83-B, No. 2, February 2000, pp. 1-10	
44.	M.J.G. van Uitert, S.C. Borst, "A Reduced-Load Equivalence For Generalized Processor Sharing Networks With Heavy-Tailed Input Flows", Probability, Networks and Algorithms (PNA), PNA-R007, August 31,	

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